

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Cancelled) An integrated circuit comprising:
an interface to a bus; and
configuration logic to transmit a query message through the bus and directed at another integrated circuit to query for availability of a hardware feature within the other integrated circuit, and to access the hardware feature within the other integrated circuit if a reply message is received from the other integrated circuit providing an indication of availability of the hardware feature within the other integrated circuit.
2. (Currently Amended) The integrated circuit of claim 7 ~~4~~, wherein the interface is able to be coupled to a point-to-point bus having at least one serial data link across which the query message is transferred in packetized form.
3. (Currently Amended) The integrated circuit of claim 2, wherein the query message transmitted by the configuration logic is a vendor-specific message conforming to support provided by the bus for a ~~the~~ transmission of the a vendor-specific message.
4. (Currently Amended) The integrated circuit of claim 7 ~~4~~, wherein the hardware feature for which the integrated circuit queries for availability is a DMA controller.

5. (Currently Amended) The integrated circuit of claim 7 ~~4~~, wherein the configuration logic accesses the hardware feature within the other integrated circuit using an address provided by the other integrated circuit within the reply message.

6. (Original) The integrated circuit of claim 5, wherein the configuration logic accesses the hardware feature to arbitrate for an opportunity to interact with the hardware feature.

7. (Currently Amended) ~~The integrated circuit of claim 1,~~ An integrated circuit comprising:

an interface to a bus; and

configuration logic to transmit a query message through the bus and directed at another integrated circuit to query for availability of a hardware feature within the other integrated circuit, and to access the hardware feature within the other integrated circuit if a reply message is received from the other integrated circuit providing an indication of availability of the hardware feature within the other integrated circuit, wherein the configuration logic transmits a version number within the query message identifying a the version of the integrated circuit.

8. (Currently Amended) ~~The integrated circuit of claim 1,~~ An integrated circuit comprising:

an interface to a bus; and

configuration logic to transmit a query message through the bus and directed at another integrated circuit to query for availability of a hardware feature within the other integrated circuit, and to access the hardware feature within the other integrated circuit if a reply message is received from the other integrated circuit providing an indication of availability of the hardware feature within the other integrated circuit, wherein the

configuration logic transmits a version number within the query message identifying a the version of the hardware feature that the integrated circuit seeks.

9. (Cancelled) An integrated circuit comprising:

an interface to a bus;
a hardware feature to interact within another integrated circuit across the bus; and
configuration logic to receive a query message through the bus and directed at the integrated circuit to query for availability of the hardware feature, and to selectively reply to the query message by transmitting with a reply message providing an indication of availability of the hardware feature.

10. (Currently Amended) The integrated circuit of claim 15 9, wherein the interface is able to be coupled to a point-to-point bus having at least one serial data link across which the query and reply messages are transferred in packetized form.

11. (Currently Amended) The integrated circuit of claim 10, wherein the reply message transmitted by the configuration logic is a vendor-specific message conforming to support provided by the bus for a the transmission of the a vendor-specific message.

12. (Currently Amended) The integrated circuit of claim 15 9, wherein the hardware feature for which the other integrated circuit queries for availability is a DMA controller.

13. (Currently Amended) The integrated circuit of claim 15 9, wherein the configuration logic provides an address within the reply message that the other integrated circuit could use to access the hardware feature.

14. (Original) The integrated circuit of claim 13, wherein the hardware feature supports being accessed by the other integrated circuit to arbitrate for an opportunity to interact with the hardware feature.

15. (Currently Amended) ~~The integrated circuit of claim 9,~~ An integrated circuit comprising:

an interface to a bus;

a hardware feature to interact within another integrated circuit across the bus; and

configuration logic to receive a query message through the bus and directed at the integrated circuit to query for availability of the hardware feature, and to selectively reply to the query message by transmitting with a reply message providing an indication of availability of the hardware feature, wherein the configuration logic transmits a version number within the reply message identifying a the version of the hardware feature.

16. (Currently Amended) ~~The integrated circuit of claim 9,~~ An integrated circuit comprising:

an interface to a bus;

a hardware feature to interact within another integrated circuit across the bus; and

configuration logic to receive a query message through the bus and directed at the integrated circuit to query for availability of the hardware feature, and to selectively reply to the query message by transmitting with a reply message providing an indication of availability of the hardware feature, wherein the configuration logic transmits a version number within the reply message identifying a version of the hardware feature that the hardware feature within the integrated circuit mimics.

17. (Cancelled) An electronic device comprising:

a bus;

a device coupled to the bus having a first configuration logic to carry out a query transaction on the bus to query for the availability of a hardware feature;

a system logic having the hardware feature, and having a second configuration logic to receive the query transaction and to selectively respond with an indication of availability of the hardware feature; and

a processor coupled to the system logic.

18. (Cancelled) The electronic system of claim 17, wherein the bus supports the transfer of a vendor-defined message, and the query transaction is a vendor-defined query message transmitted across the bus.

19. (Currently Amended) The electronic system of claim 20 ~~18~~, wherein the bus is a point-to-point bus having at least one serial data link across which the query message is transferred in packetized form.

20. (Currently Amended) ~~The electronic system of claim 18;~~ An electronic device comprising:

a bus,;

a device coupled to the bus having a first configuration logic to carry out a query transaction on the bus to query for the availability of a hardware feature;

a system logic having the hardware feature, and having a second configuration logic to receive the query transaction and to selectively respond with an indication of availability of the hardware feature; and

a processor coupled to the system logic,

wherein the bus supports the transfer of a vendor-defined message, and the query transaction is a vendor-defined query message transmitted across the bus, and further wherein the first configuration logic transmits a version number within the query message indicating a the version of the hardware feature that the device is able to interact with.

21. (Currently Amended) ~~The electronic system of claim 18,~~ An electronic device comprising:

a bus,;

a device coupled to the bus having a first configuration logic to carry out a query transaction on the bus to query for the availability of a hardware feature;

a system logic having the hardware feature, and having a second configuration logic to receive the query transaction and to selectively respond with an indication of availability of the hardware feature; and

a processor coupled to the system logic,

wherein the bus supports the transfer of a vendor-defined message, and the query transaction is a vendor-defined query message transmitted across the bus, and further wherein the second configuration logic receives a version number within the query message from the first configuration logic that the second configuration logic analyzes to determine whether to respond to the query message.

22. (Currently Amended) ~~The electronic system of claim 18,~~ An electronic device comprising:

a bus,;

a device coupled to the bus having a first configuration logic to carry out a query transaction on the bus to query for the availability of a hardware feature;

a system logic having the hardware feature, and having a second configuration logic to receive the query transaction and to selectively respond with an indication of availability of the hardware feature; and

a processor coupled to the system logic,
wherein the bus supports the transfer of a vendor-defined message, and the query transaction is a vendor-defined query message transmitted across the bus, and further
wherein the first configuration logic transmits a code identifying a vendor within the query message from the first configuration logic that the second configuration logic analyzes to determine whether to respond to the query message.

23. (Cancelled) The electronic system of claim 17, wherein the bus supports the transfer of a vendor-defined message, and the second configuration logic responds to the query transaction with a is a vendor-defined reply message transmitted across the bus.

24. (Currently Amended) The electronic system of claim 25 23, wherein the bus is a point-to-point bus having at least one serial data link across which the reply message is transferred in packetized form.

25. (Currently Amended) ~~The electronic system of claim 23,~~ An electronic device comprising:

a bus,;

a device coupled to the bus having a first configuration logic to carry out a query transaction on the bus to query for the availability of a hardware feature;

a system logic having the hardware feature, and having a second configuration logic to receive the query transaction and to selectively respond with an indication of availability of the hardware feature; and

a processor coupled to the system logic, wherein the bus supports the transfer of a vendor-defined message, and the second configuration logic responds to the query transaction with a is a vendor-defined reply message transmitted across the bus, and further wherein the second configuration logic transmits a version number within the reply message indicating a the version of the hardware feature that the system logic possesses.

26. (Original) ~~The electronic system of claim 23,~~ An electronic device comprising:

a bus,;

a device coupled to the bus having a first configuration logic to carry out a query transaction on the bus to query for the availability of a hardware feature;

a system logic having the hardware feature, and having a second configuration logic to receive the query transaction and to selectively respond with an indication of availability of the hardware feature; and

a processor coupled to the system logic, wherein the bus supports the transfer of a vendor-defined message, and the second configuration logic responds to the query transaction with a is a vendor-defined reply message transmitted across the bus, and further wherein the first configuration logic receives a version number within the reply message from the second configuration logic that the first configuration logic analyzes to determine whether to interact with the hardware feature.

27. (Currently Amended) The electronic system of claim 25 ~~17~~, wherein the bus is one of a plurality of buses organized into a branching tree-like structure with the system logic at the base tree and designated as the root complex.

28. (Original) The electronic system of claim 27, wherein the system logic is coupled to the bus, the bus supports the transfer of a message as the query transaction to query for the hardware feature, and the message is directed to the system logic through an indication provided in the message that the message is directed to the device on the other end of whichever bus is coupled to the device that leads towards the root complex.

29. (Original) The electronic system of claim 27, wherein the bus supports the transfer of a message as the query transaction to query for the hardware feature, and the message is directed to the system logic through an indication provided in the message that the message is directed to a device at the root complex.

30. (Cancelled) A method comprising:

transmitting a query message by logic within a first device across a bus to a second device to query for the availability of a hardware feature within the second device;

receiving the query message by logic within the second device; and

selectively responding by the logic within the second device to the query message by transmitting a reply message providing an indication of availability of the hardware feature within the second device.

31. (Currently Amended) ~~The method of claim 30, further comprising:~~ A method comprising:

transmitting a query message by logic within a first device across a bus to a second device to query for the availability of a hardware feature within the second device;

receiving the query message by logic within the second device;

selectively responding by the logic within the second device to the query message by transmitting a reply message providing an indication of availability of the hardware feature within the second device;

transmitting a version number within the query message indicating ~~a~~ the version of the hardware feature that the first device seeks to interact with; and

analyzing by the logic within second device of the version number transmitted within the query message to determine whether to transmit the reply message in response.

32. (Currently Amended) ~~The method of claim 30, further comprising:~~ A method comprising:

transmitting a query message by logic within a first device across a bus to a second device to query for the availability of a hardware feature within the second device;

receiving the query message by logic within the second device;

selectively responding by the logic within the second device to the query message by transmitting a reply message providing an indication of availability of the hardware feature within the second device;

transmitting a code identifying a vendor within the query message indicating ~~a~~ the version of the hardware feature that the first device seeks to interact with; and

analyzing by the logic within the second device of the code identifying the vendor transmitted within the query message to determine whether to transmit the reply message in response.

33. (Currently Amended) ~~The method of claim 30, further comprising:~~ A method comprising:

transmitting a query message by logic within a first device across a bus to a second device to query for the availability of a hardware feature within the second device;

receiving the query message by logic within the second device;

selectively responding by the logic within the second device to the query message by transmitting a reply message providing an indication of availability of the hardware feature within the second device;

transmitting a version number within the reply message indicating ~~a~~ the version of the hardware feature possessed by the second device; and

analyzing by the logic within the first device of the version number transmitted within the reply message to determine whether to interact with the hardware feature.

34. (Cancelled) The method of claim 30, further comprising transmitting by the logic within the second device of an address within the reply message to the logic within the first device, the address specifying an address location at which the hardware feature is accessible.

35. (Original) The method of claim 33, further comprising arbitrating by the logic within the first device to gain an opportunity to interact with the hardware feature within the second device if the determination is made to interact with the hardware feature.